

**IN THE CLAIMS:**

The text of all pending claims (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

1. (Currently Amended) An interface device for performing data transmission with a further device ~~connected~~ coupled to a network at any of a plurality of transmission rates that are regulated, the interface device comprising:

a transmission rate control circuit configured to ~~change~~ generate a switch signal that changes an operation speed of the interface device when the transmission rate must be switched; and

a clock generation circuit configured to change a frequency in response to the switch signal and generate a clock signal having the changed frequency.

2. (Original) The interface device of claim 1, wherein the switching of the transmission rate is executed when data transmission to the further device is required or when a request to switch to a different transmission rate is received from the further device.

3 (Original) The interface device of claim 1, wherein the transmission rate control circuit switches to a transmission rate enabling low-speed transmission during low-speed transmission and switches to a transmission rate enabling high-speed transmission when high-speed transmission is required.

4. (Original) The interface device of claim 1, wherein the transmission rate control circuit switches to a transmission rate enabling minimum speed transmission operation when starting operation for connection to the network or when data is not being transmitted.

5. (Previously Presented) The interface device of claim 1, further comprising a register for storing among the plurality of transmission rates, a transmission capacity of the interface device itself, a transmission rate that is presently possible, and a transmission rate to be switched to next.

6. (Previously Presented) The interface device of claim 5, wherein the register stores information for a mode for maintaining the present transmission rate or information for a mode for switching to a transmission rate enabling the minimum speed transmission operation.

7. (Previously Presented) The interface device of claim 6, wherein the information stored in the register is changeable by a bus reset.

8. (Currently Amended) A method for controlling an interface device for performing data transmission with other devices-~~connected~~ coupled to a network at any of a plurality of transmission rates that are regulated, the method comprising:

providing the interface device and each device that are configured to change its own operation speed; and

changing operation speeds of each device and the interface device from a low-speed transmission rate to a high-speed transmission rate when switching to a high-speed transmission rate is required and each device included in a route to a transmission destination is compatible for the high-speed transmission,

wherein the interface device includes a transmission rate control circuit and a clock generation circuit, and said changing operation speeds of each device and the interface device includes:

the transmission rate control circuit generating a switch signal that changes an operation speed of the interface device; and

the clock generation circuit changing a frequency in response to the switch signal to generate a clock signal having the changed frequency.

9. (Original) The method for controlling an interface device of claim 8, further comprising:

determining whether the high-speed transmission is required or not after the high-speed transmission ends;

setting information for a mode for continuing high speed transmission when the high-speed transmission is required, and setting information for a mode for switching to a transmission rate enabling minimum speed transmission operation when the high-speed transmission is not required.

10. (Previously Presented) The method for controlling an interface device of claim 9

further comprising generating a bus reset after the high-speed transmission ends, and individually changing the respective setting of the operation modes of each device and the interface device itself with the bus reset.

11. (Currently Amended) A method, comprising:  
configuring a transmission rate control circuit to change operation speed of at least one of a plurality of devices when a transmission rate must be switched; and  
changing the operation speed of the at least one of the plurality of devices based on the configured transmission rate control circuit,  
wherein said configuring the transmission rate control circuit includes configuring the transmission rate control circuit to generate a switch signal that changes the operation speed of the interface device, the method further comprising:  
configuring a clock generation circuit to change a frequency in response to the switch signal to generate a clock signal having the changed frequency.

12. (New) An interface device comprising:  
an input/output port, coupled to a network, inputting data to another interface device or outputting the data from the another interface device through the network at any of a plurality of transmission rates; and  
a transmission rate control circuit changing an operation speed of the interface device from a low speed to a high speed in the case that a switching of the transmission rate is requested, and returning an operation speed of the interface device from the high speed to the low speed in the case that an operating state of the interface device is reset.